

F i g . 1 P R I O R A R T

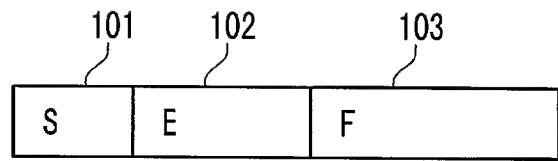


Fig. 2 PRIORITY PART

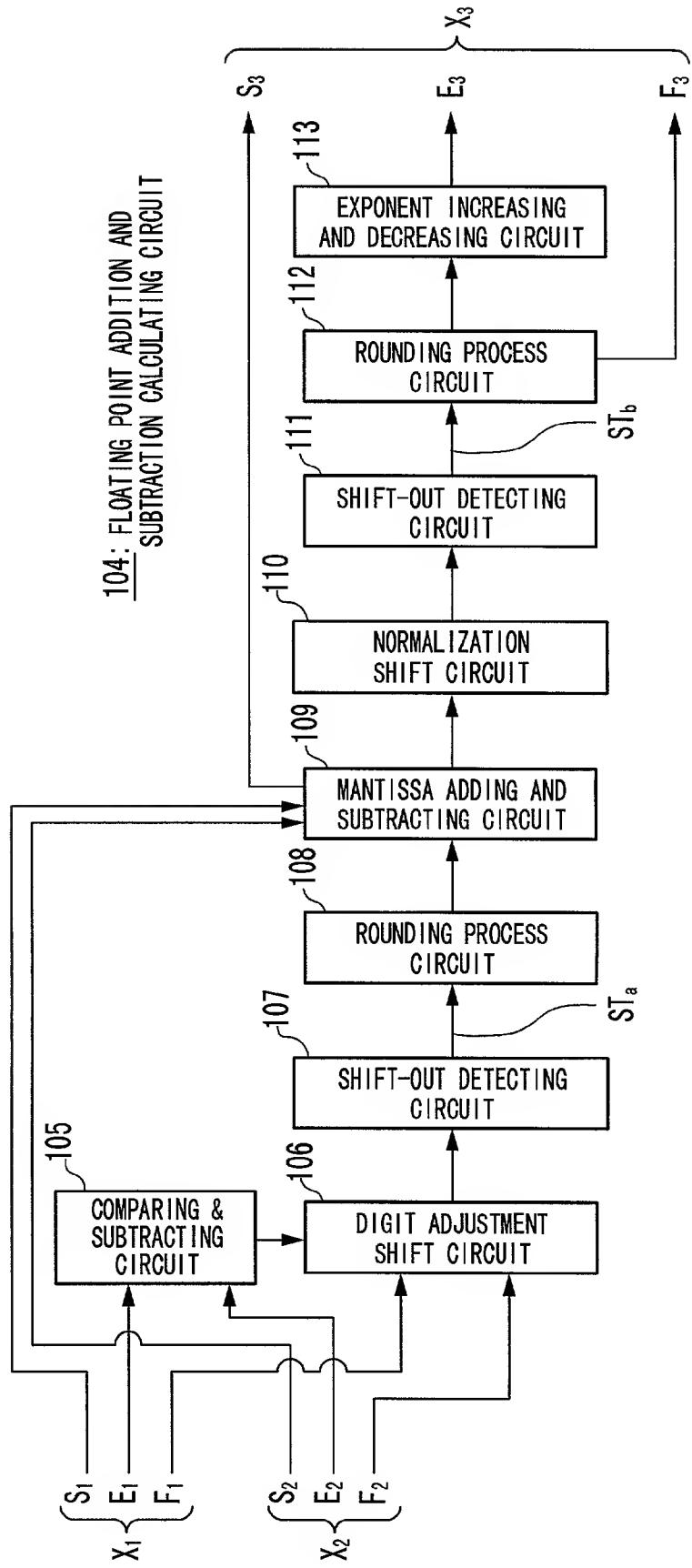
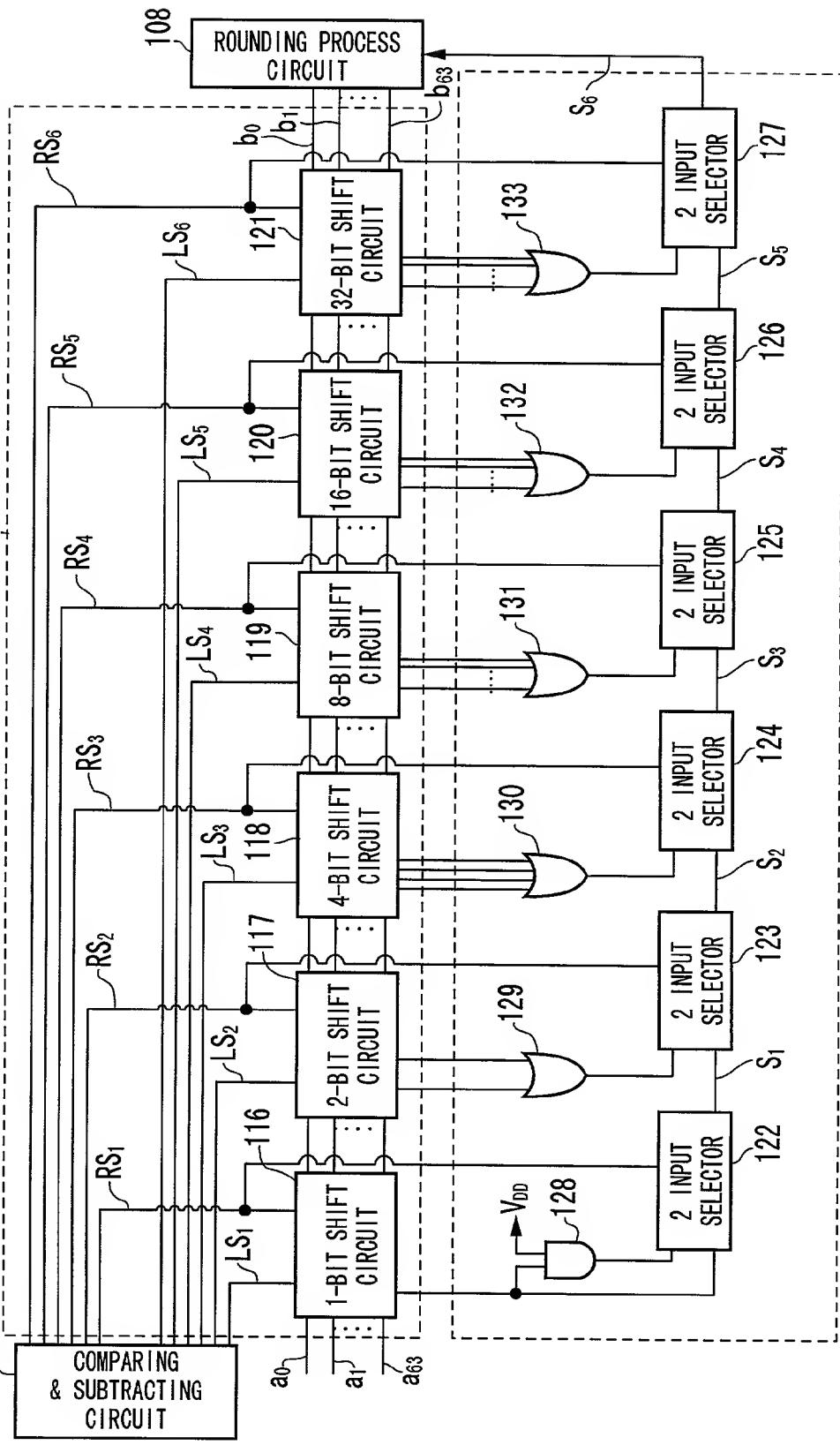


Fig. 3 P R I O R A T

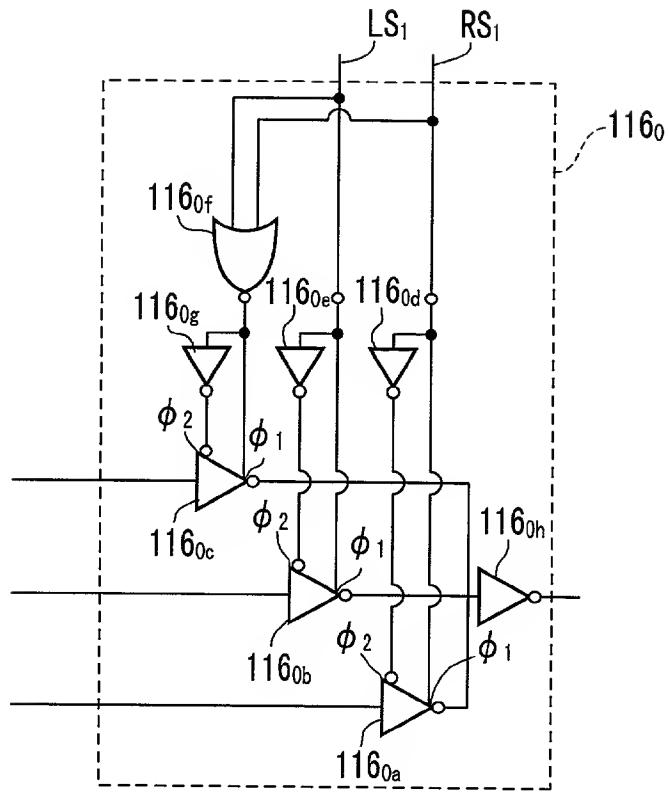
105

114: DIGIT ADJUSTMENT SHIFT CIRCUIT



115: SHIFT-OUT DETECTING CIRCUIT

F i g . 4 A P R I O R A R T



F i g . 4 B P R I O R A R T

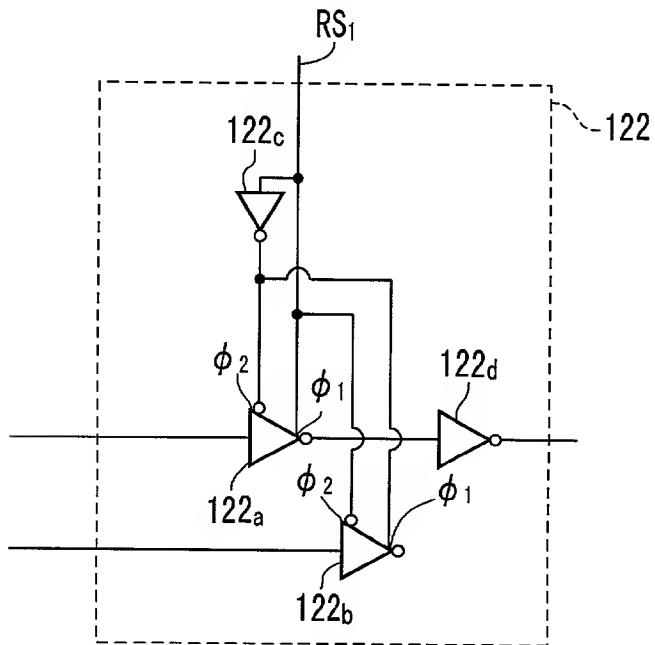
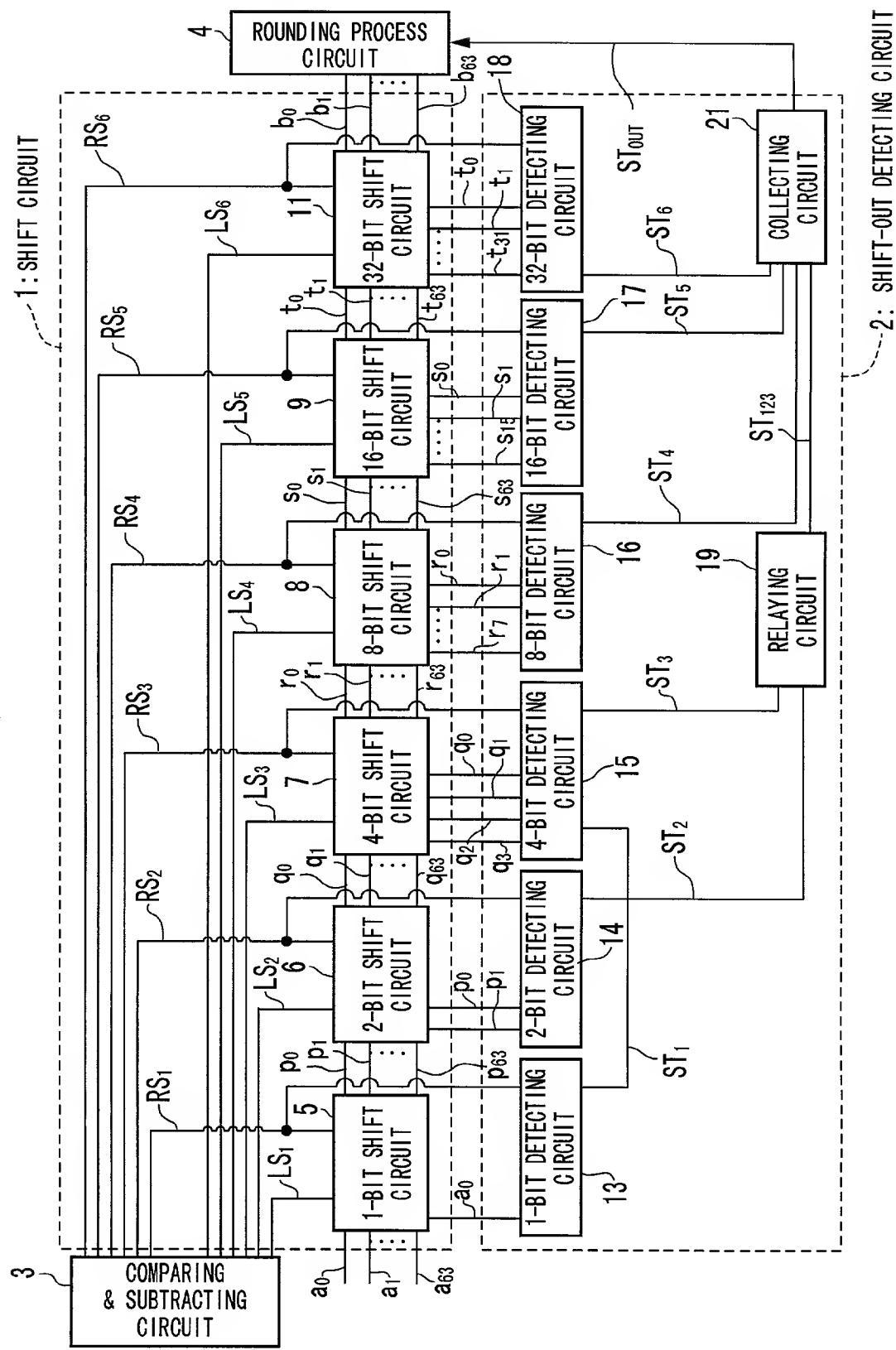


Fig. 5



6

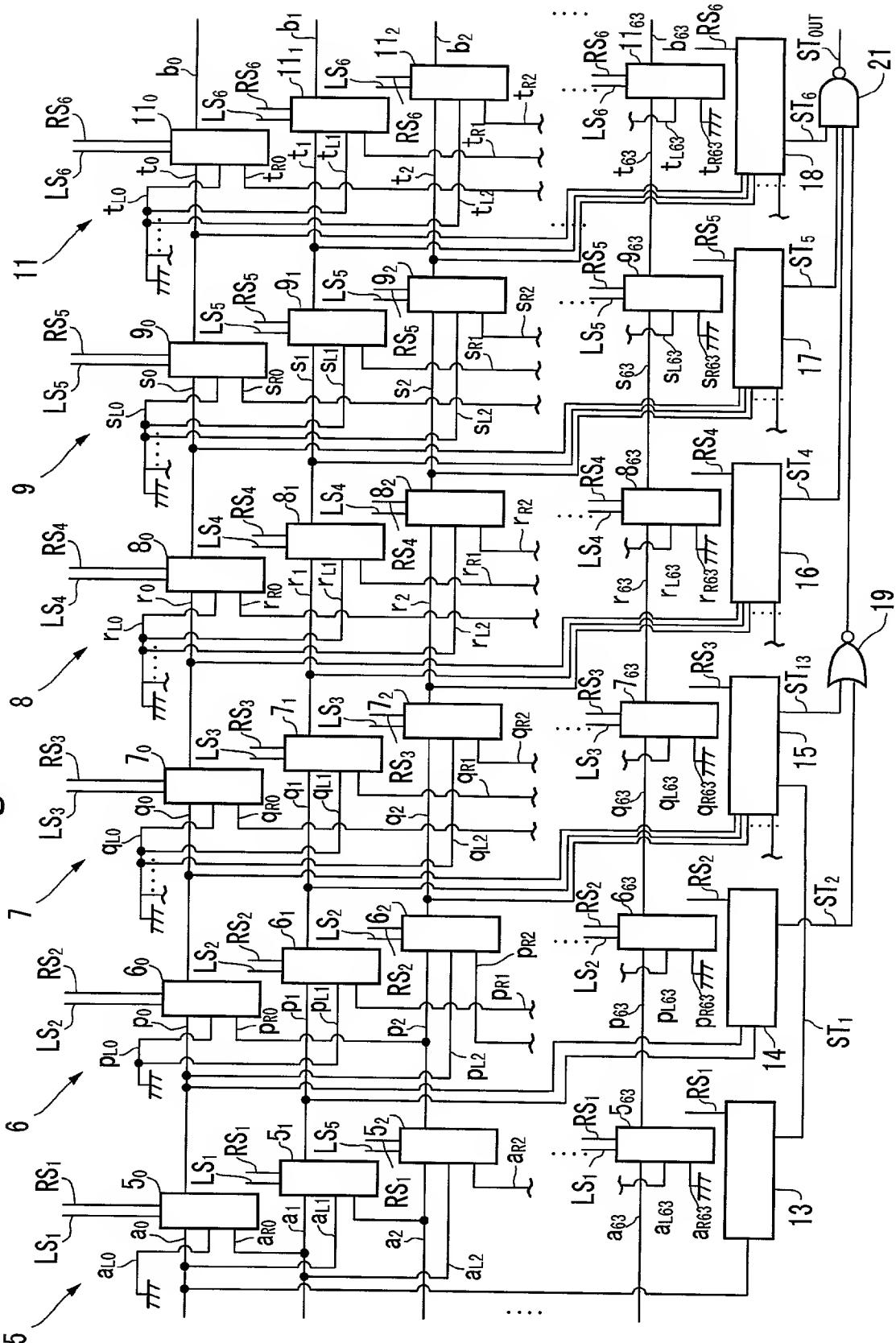


Fig. 7

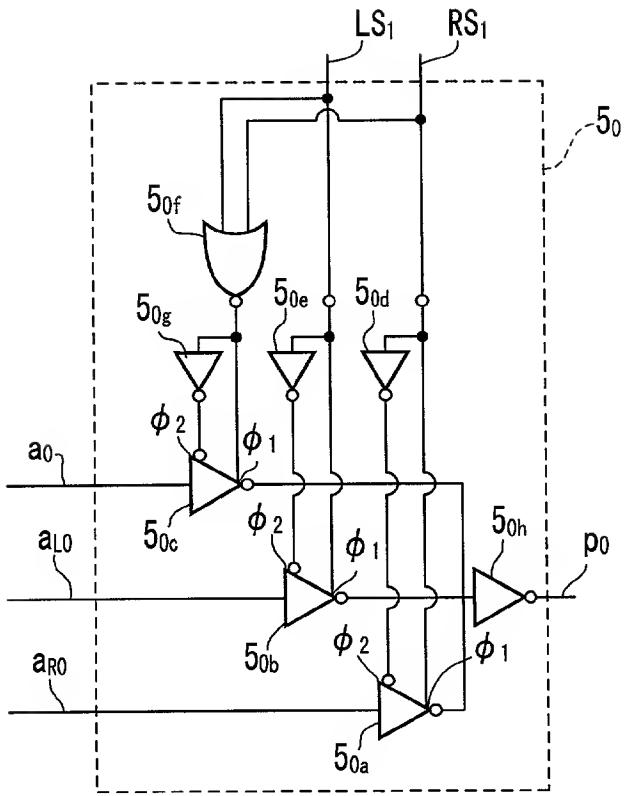


Fig. 8

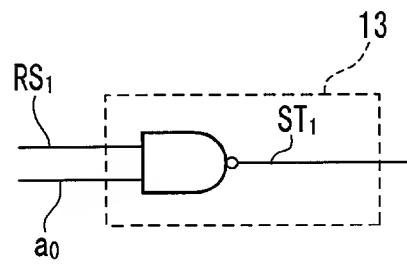


Fig. 9

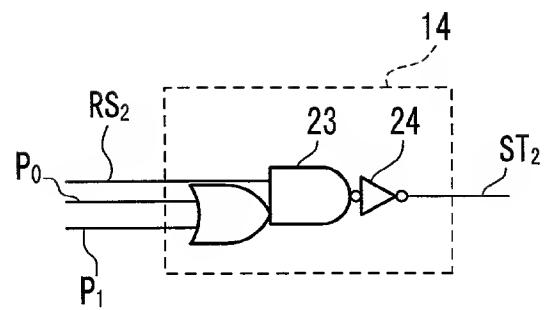
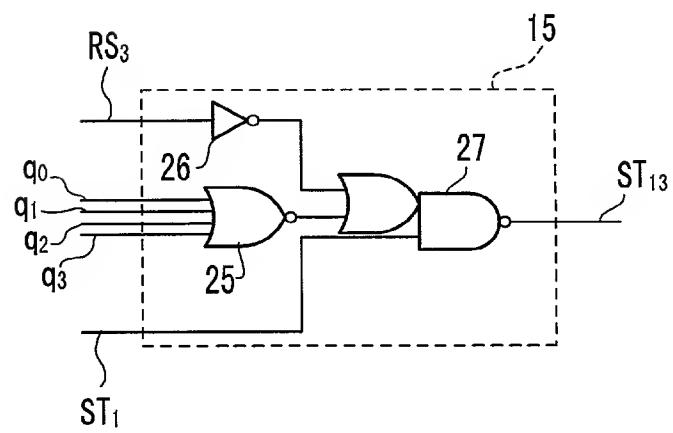
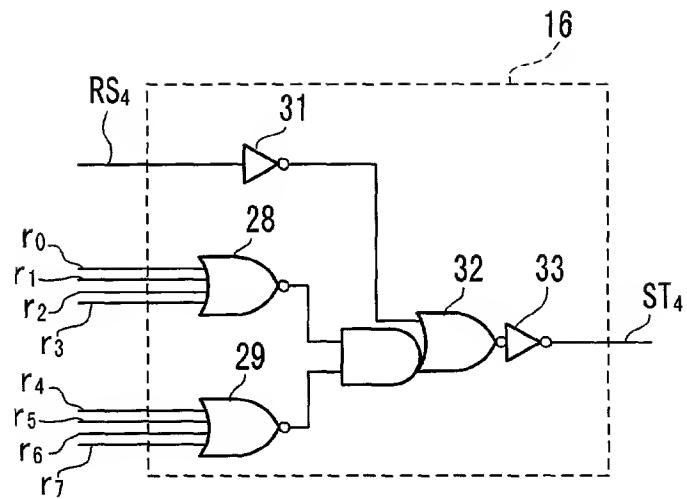


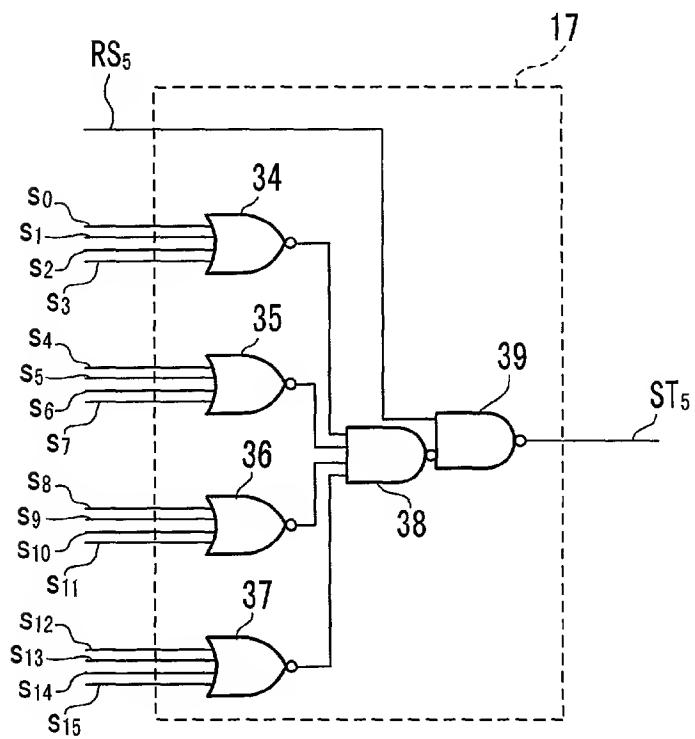
Fig. 10



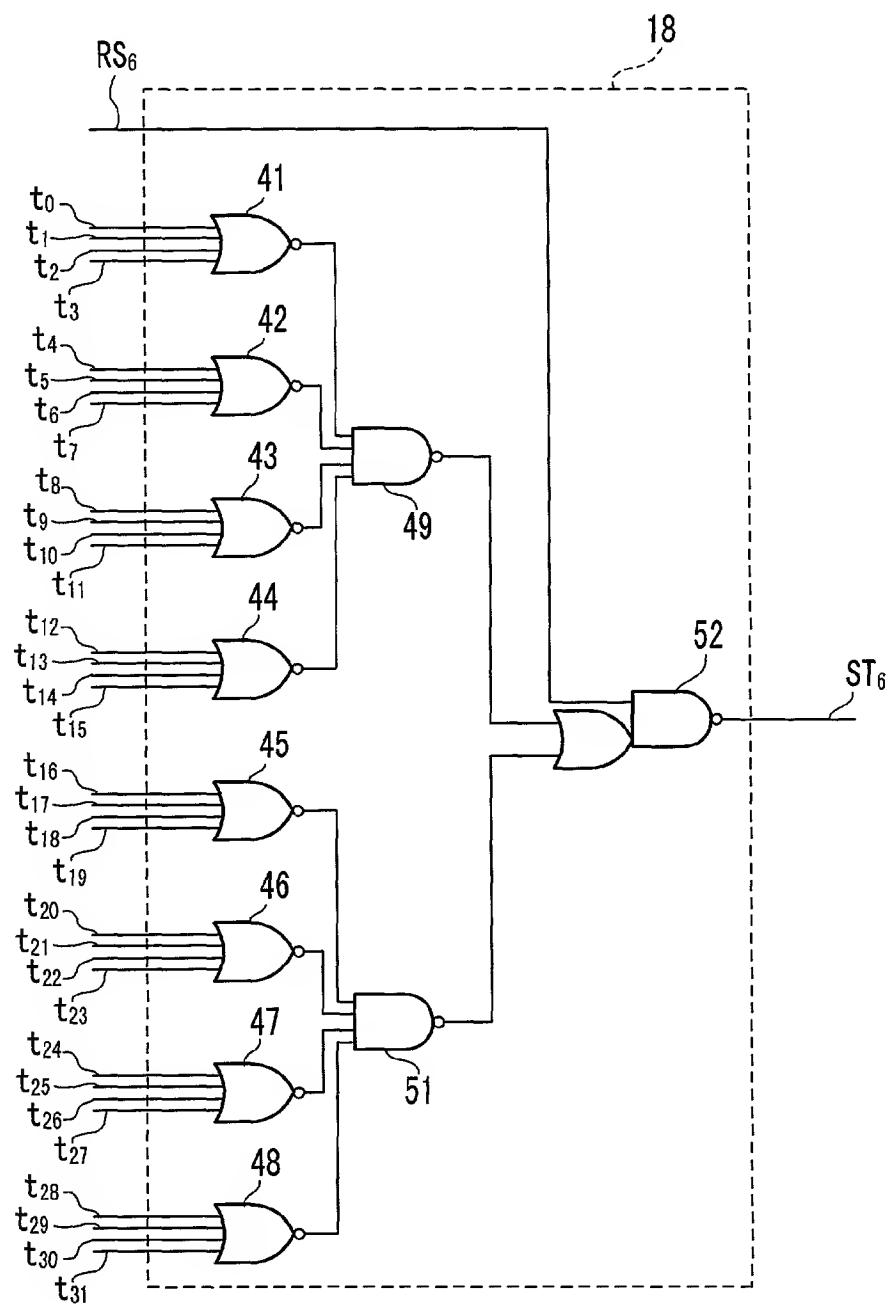
F i g . 1 1



F i g . 1 2



F i g . 1 3



F i g . 1 4 A

a ₆₃	a ₆₂	a ₆₁	a ₆₀	...	a ₃	a ₂	a ₁	a ₀
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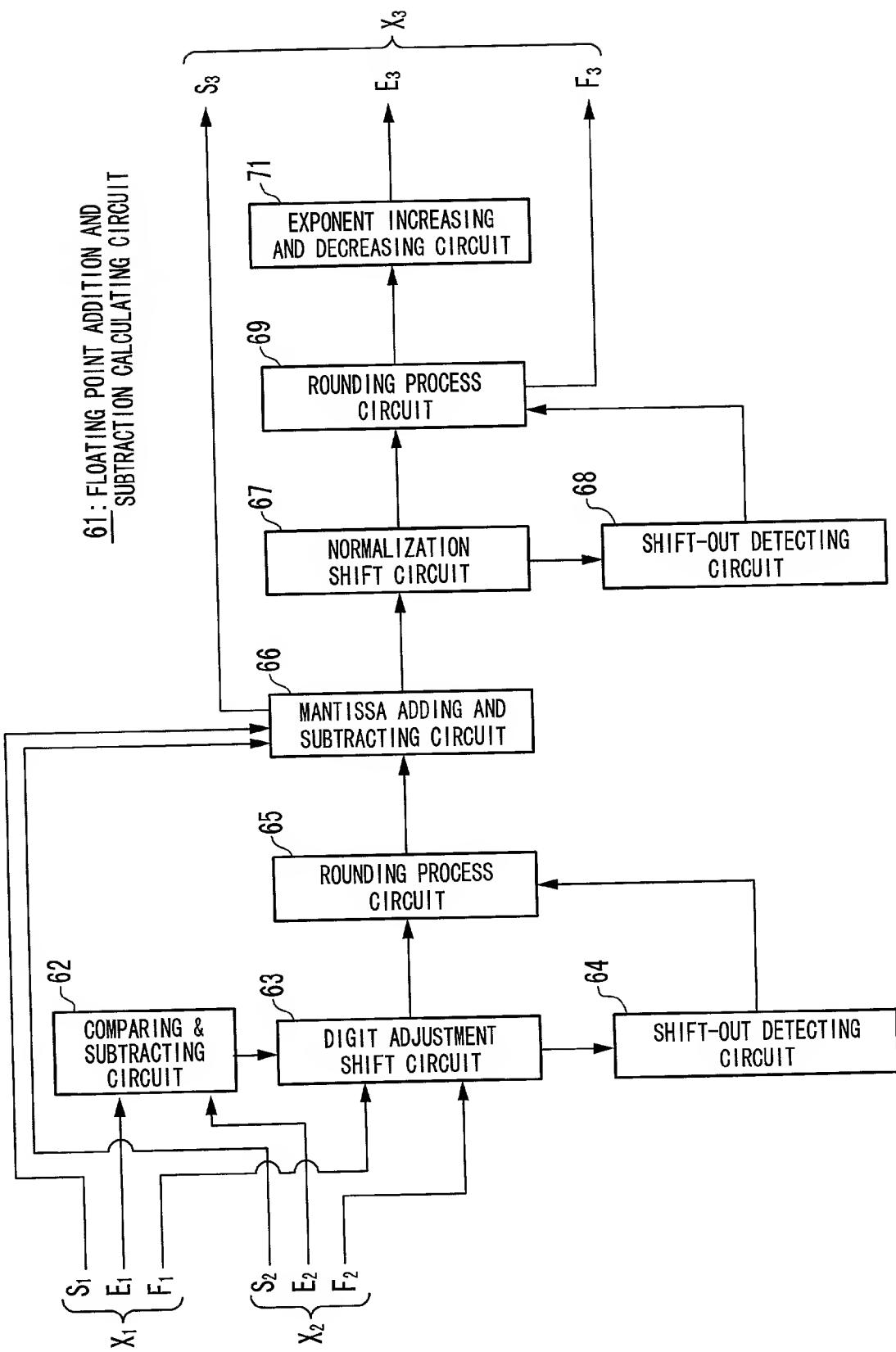
F i g . 1 4 B

0	a ₆₃	a ₆₂	a ₆₁	...	a ₄	a ₃	a ₂	a ₁	a ₀
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F i g . 1 4 C

0	0	0	a ₆₃	...	a ₆	a ₅	a ₄	a ₃	a ₂ a ₁ a ₀
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Fig. 15



F i g . 1 6

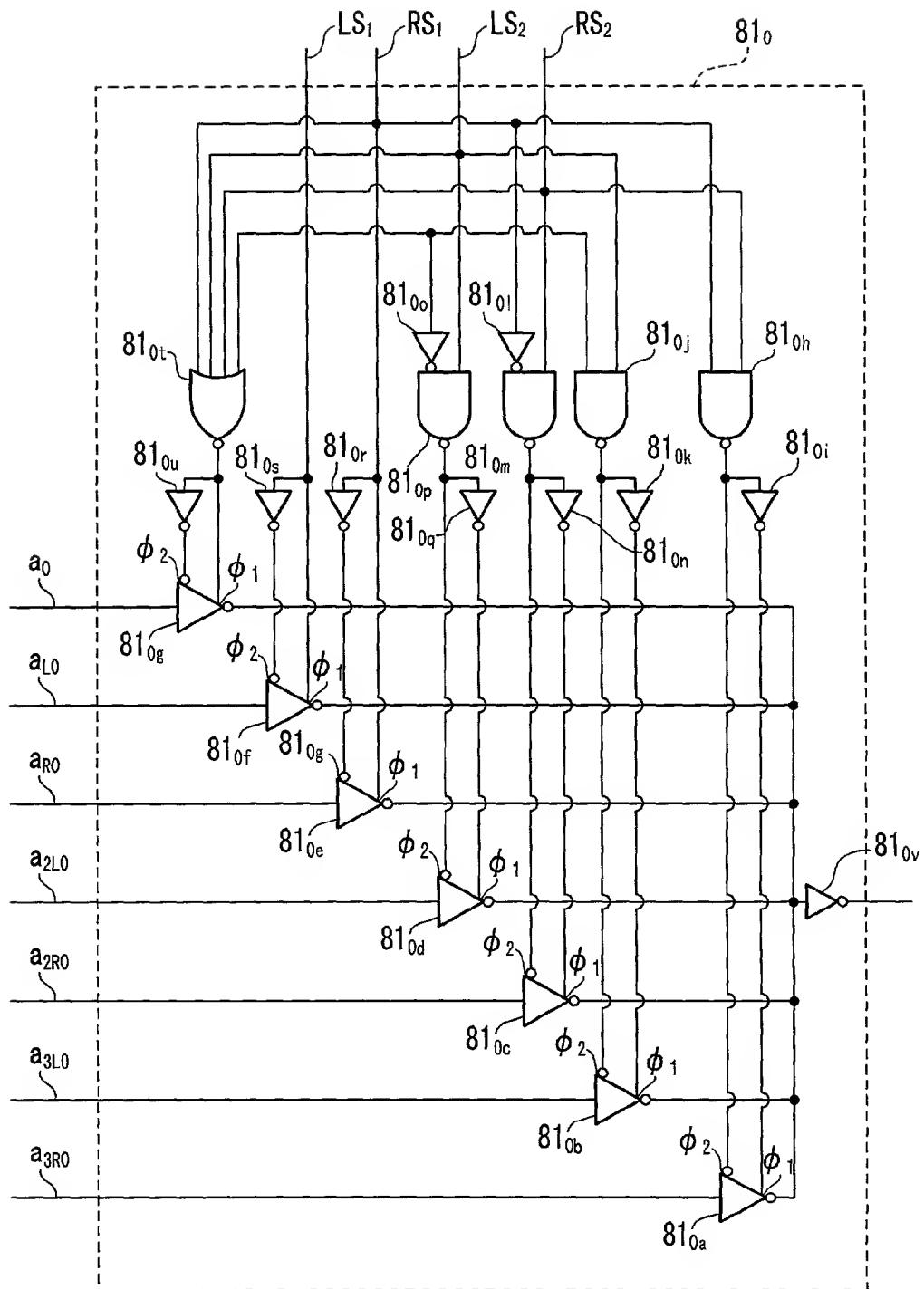


Fig. 17

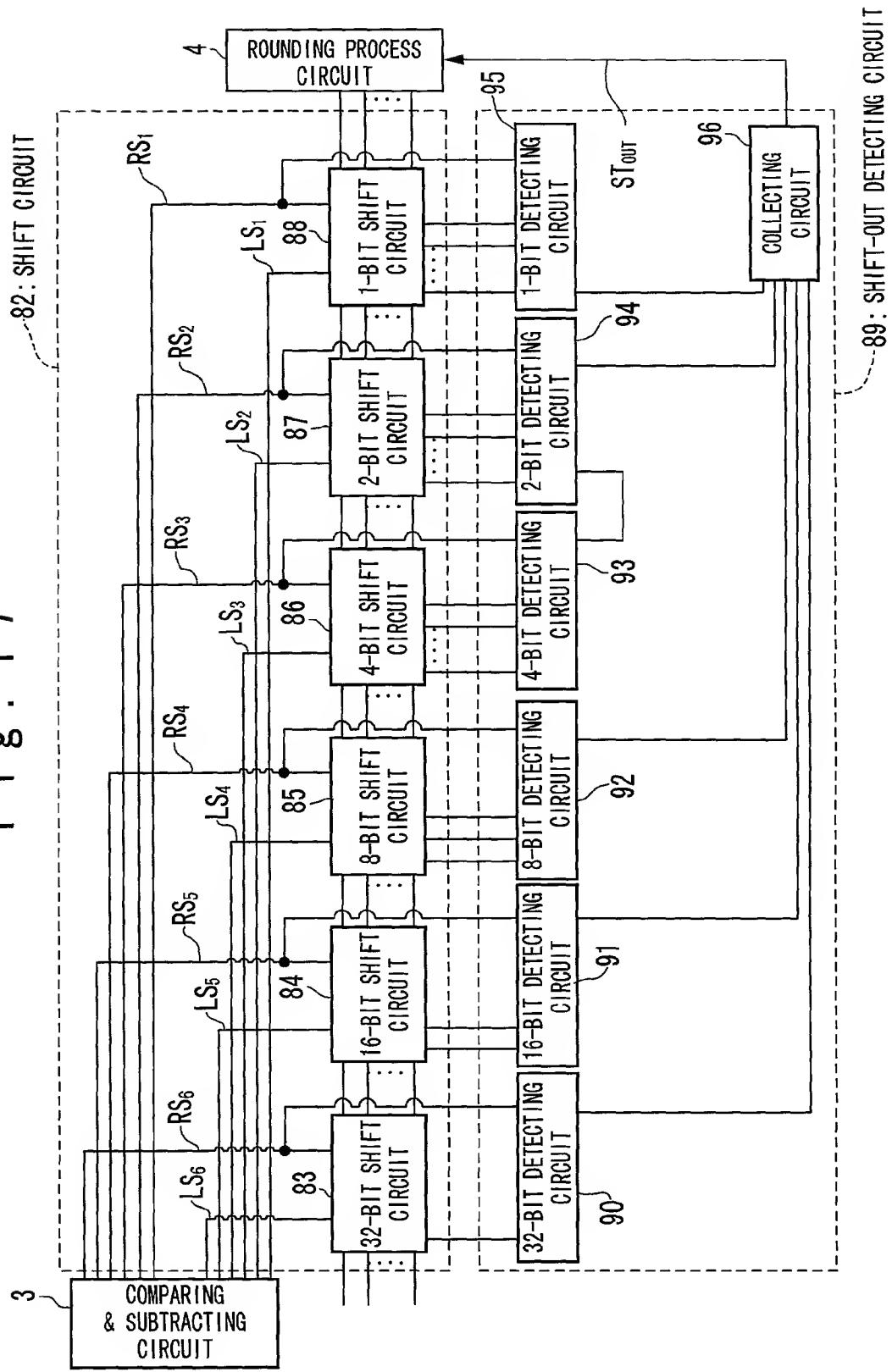
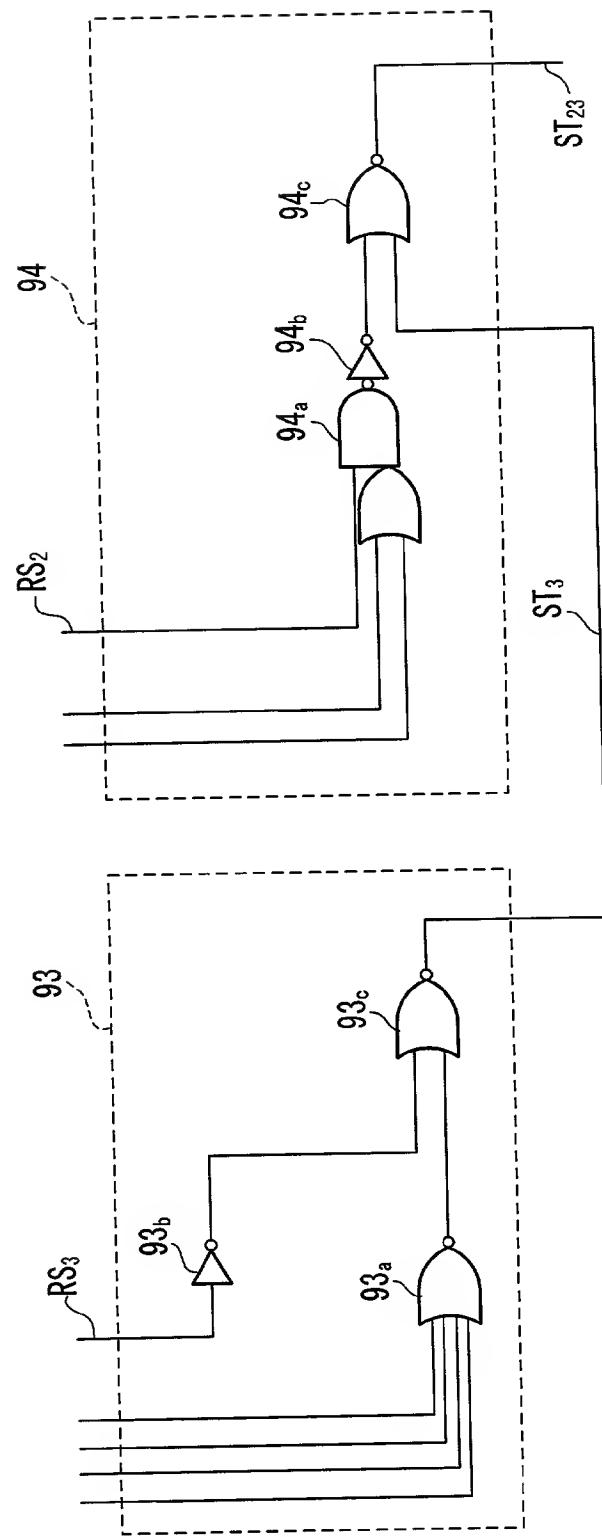
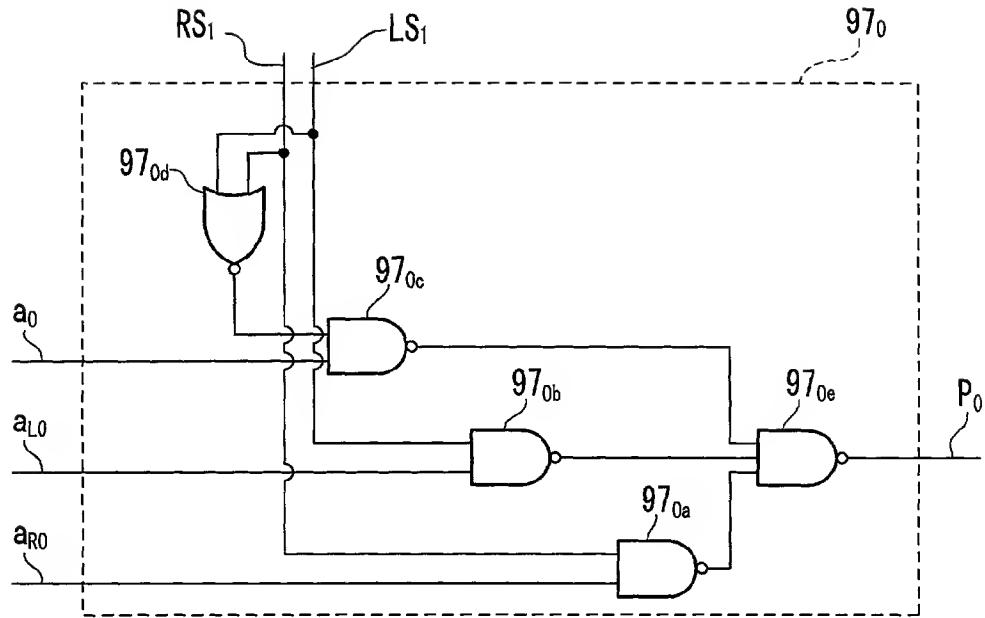


Fig. 18



F i g . 1 9



F i g . 2 0

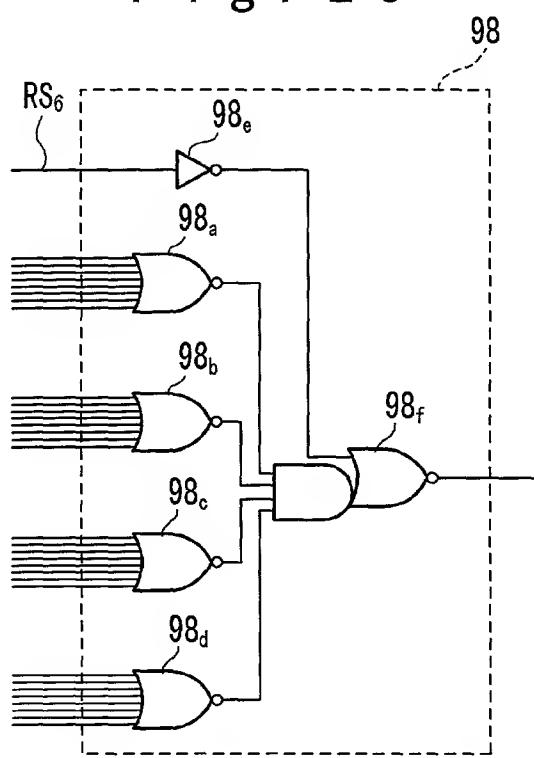


Fig. 21

